Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9921	(stress adj2 test$4) or (burn-in adj2 test$4)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 11:57
L2	1246	(stress$4 adj3 (IC or (integrated adj2 circuit$1)))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:03
L6	9	(stress$4 adj3 (IC or (integrated adj2 circuit$1))) and (first adj3 (latch$1 or flip-flop$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:03
L7	2	(stress$4 adj3 (IC or (integrated adj2 circuit$1))) and (first adj3 (latch$1 or flip-flop$1)) and (second adj2 (latch$2 or flio-flop$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:04
L8	17753	(first adj3 (latch$1 or flip-flop$1)) and (second adj2 (latch$2 or flip-flop$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:04
L9	1372	(first adj3 (latch$1 or flip-flop$1)) and (second adj2 (latch$2 or flip-flop$1)) and (first adj2 logic$1) and (second adj2 logic$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:06
L10	4	(first adj3 (latch$1 or flip-flop$1)) and (second adj2 (latch$2 or flip-flop$1)) and (first adj2 logic$1) and (second adj2 logic$1) and l1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:05
L11	21	(first adj3 (latch$1 or flip-flop$1)) and (second adj2 (latch$2 or flip-flop$1)) and (first adj2 logic$1) and (second adj2 logic$1) and LSSD	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:06
L12	21	(first adj3 (latch$1 or flip-flop$1)) and (second adj2 (latch$2 or flip-flop$1)) and (first adj2 logic$1) and (second adj2 logic$1) and (714/724.ccls.)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:07
L13	6	((first adj3 (latch$1 or flip-flop$1)) same (second adj2 (latch$2 or flip-flop$1)) same (first adj2 logic$1) same (second adj2 logic$1)) and (714/724.ccls.)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/01/22 12:08